

FIG. 1

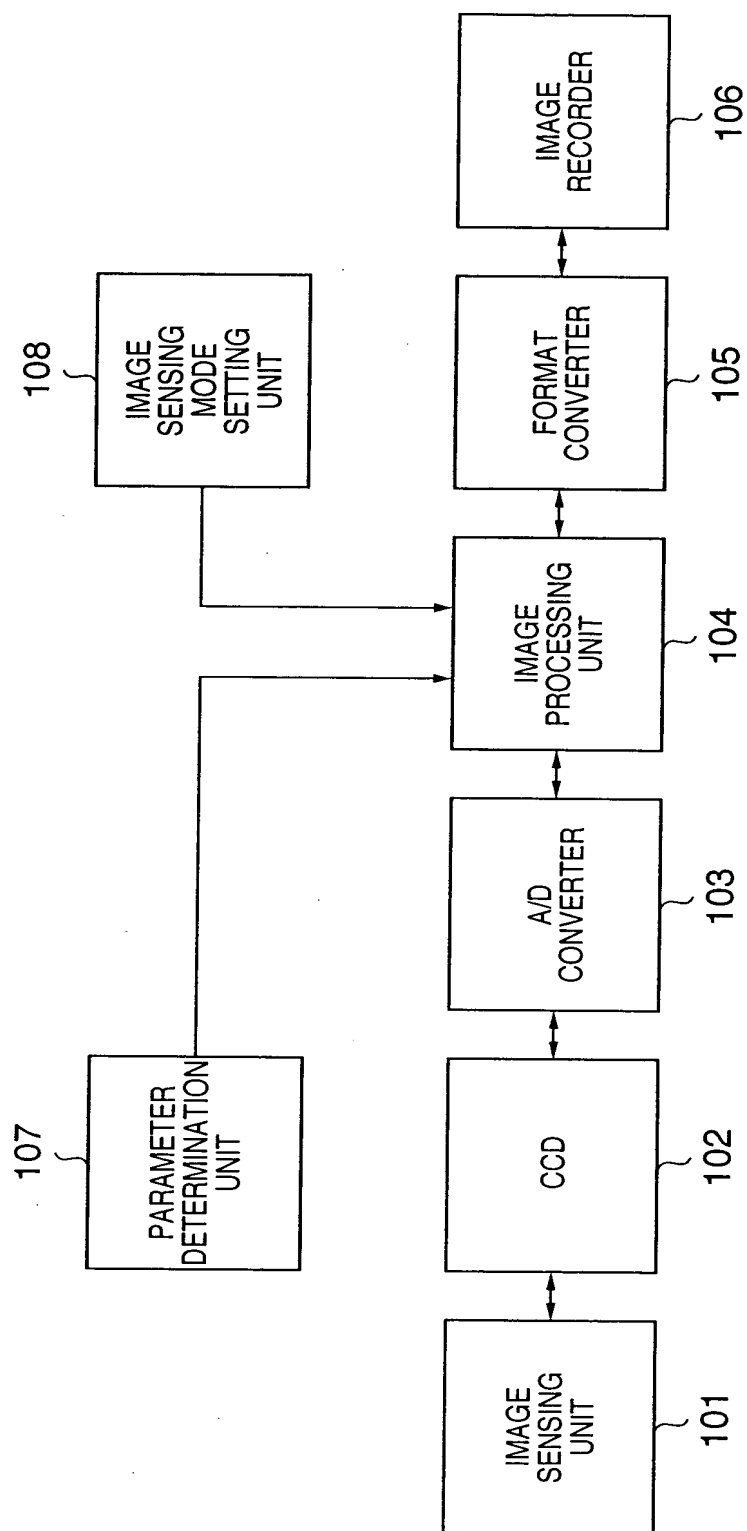


FIG. 2

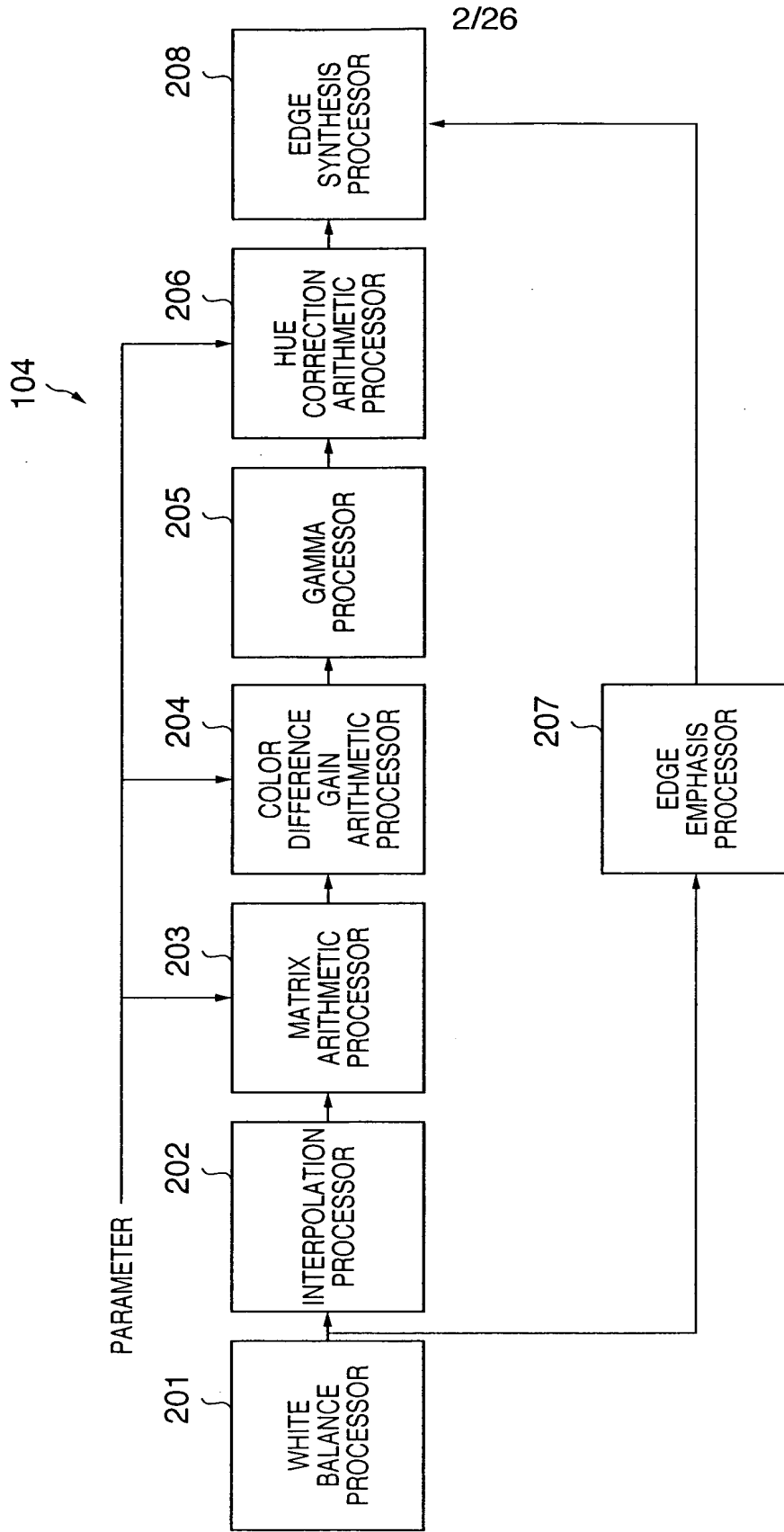
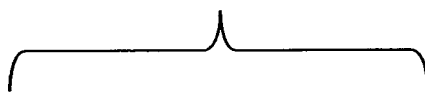


FIG. 3


300

R	G1	R	G1	R	G1
G2	B	G2	B	G2	B
R	G1	R	G1	R	G1
G2	B	G2	B	G2	B
R	G1	R	G1	R	G1
G2	B	G2	B	G2	B

[illegible][illegible][illegible][illegible]

FIG. 5

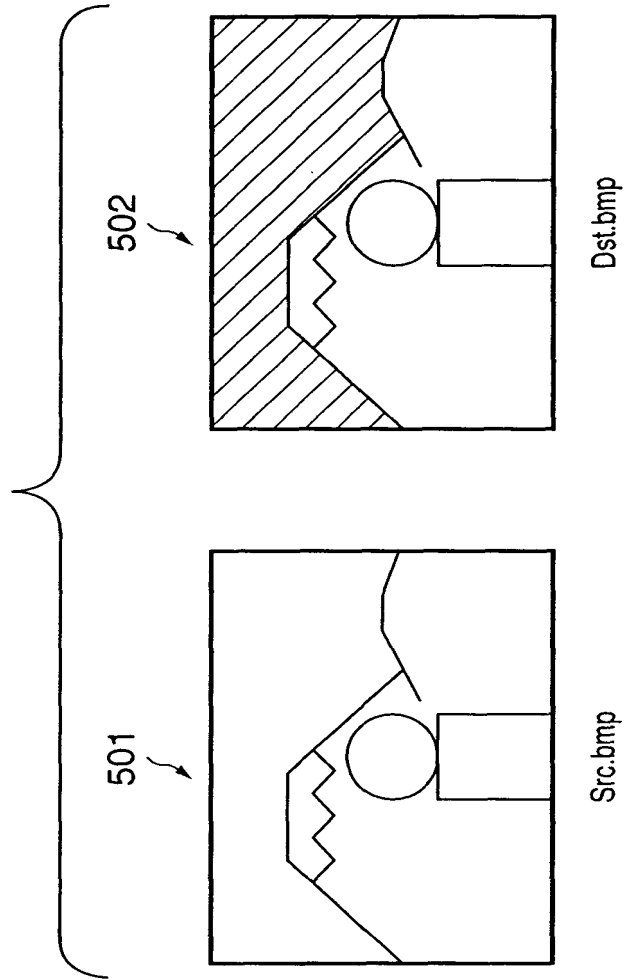


FIG. 6

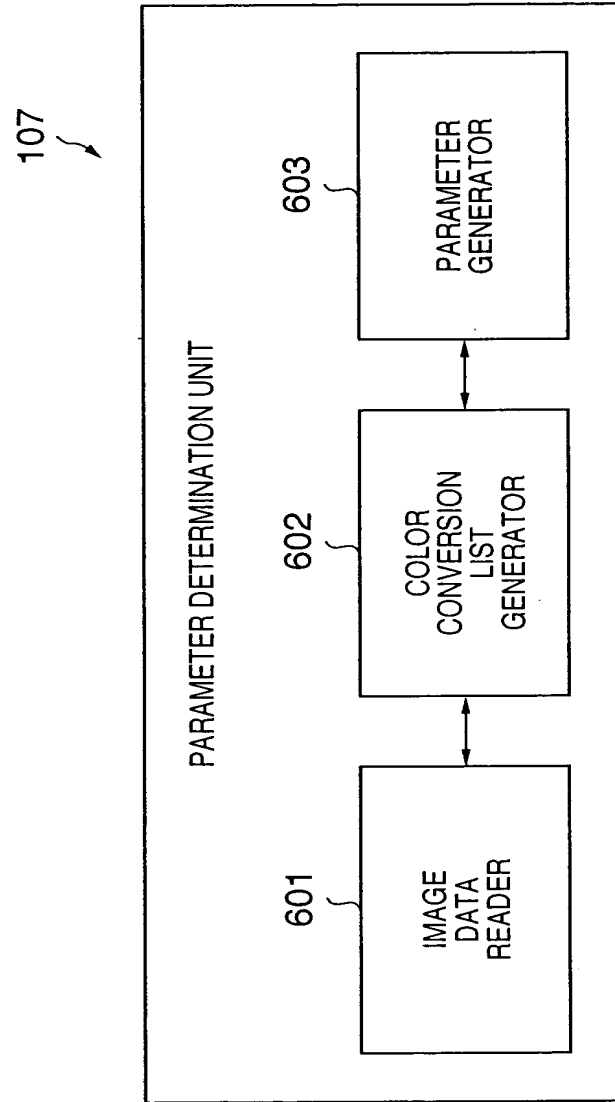


FIG. 7A

700

Src		Dst
(Rsn, Gsn, Bsn)	→	(Rdn, Gdn, Bdn)
(0, 0, 0)	→	(0, 0, 0)
(32, 0, 0)	→	(32, 0, 0)
(64, 0, 0)	→	(64, 0, 0)
.		.
(128, 192, 224)	→	(128, 180, 226)
.		.
(160, 192, 224)	→	(160, 185, 224)
.		.
(160, 224, 255)	→	(160, 212, 255)
.		.
(224, 255, 255)	→	(224, 255, 255)
.		.
(255, 255, 255)	→	(255, 255, 255)

FIG. 7B

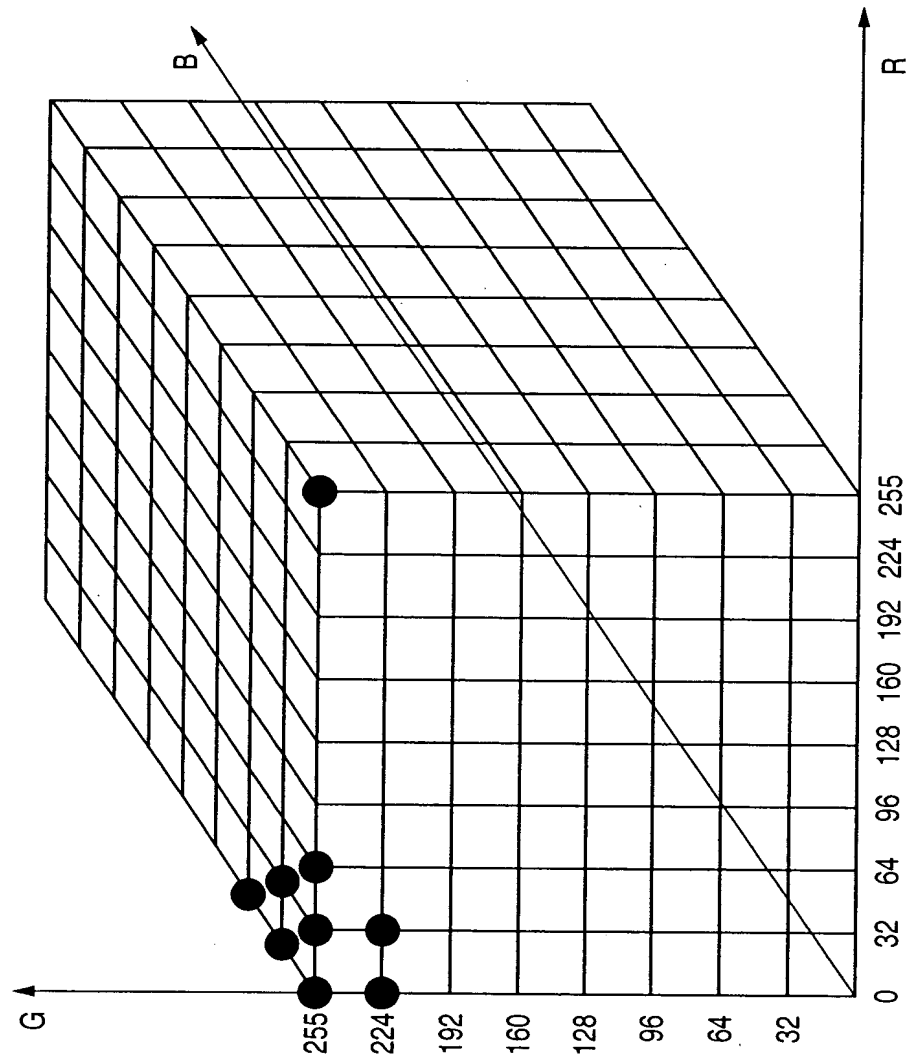


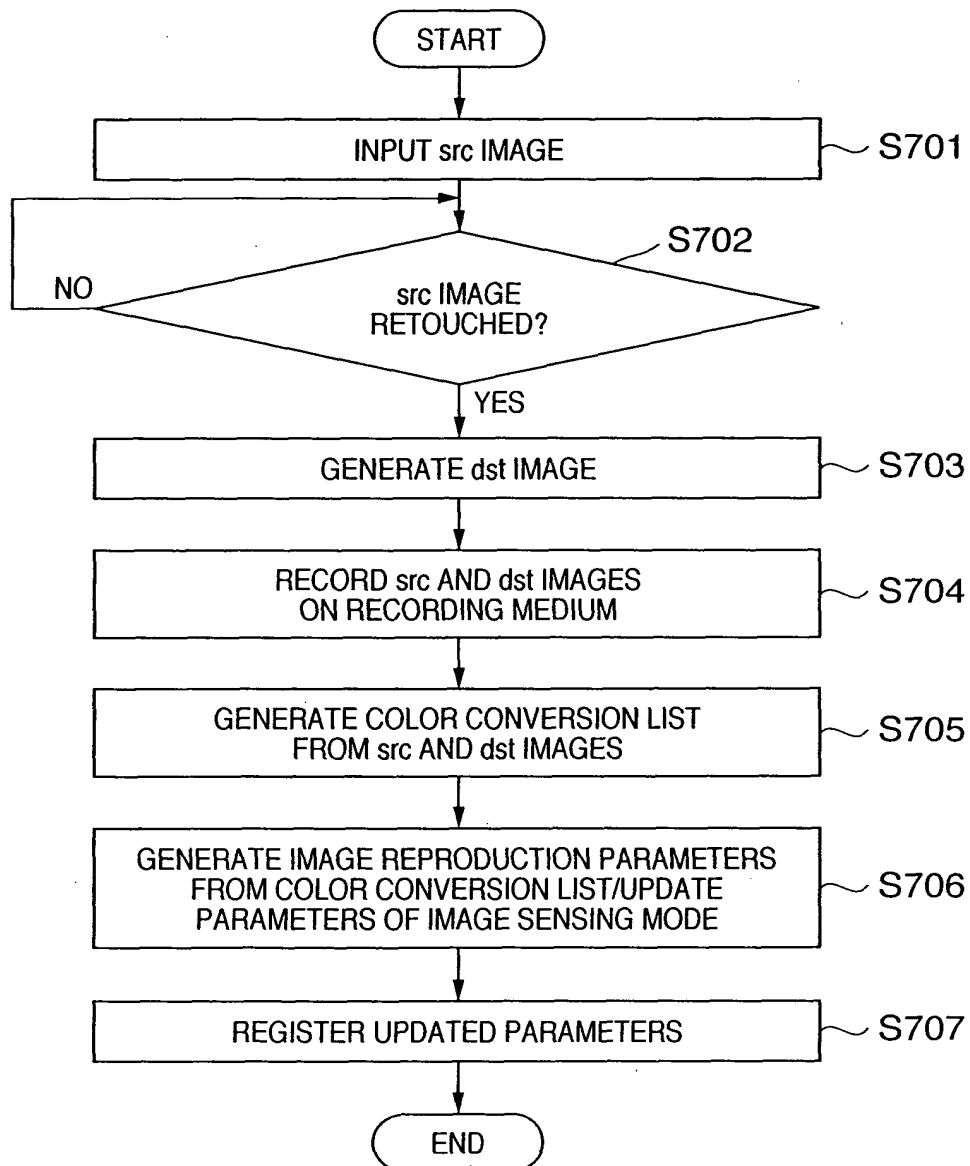
FIG. 8A

FIG. 8B

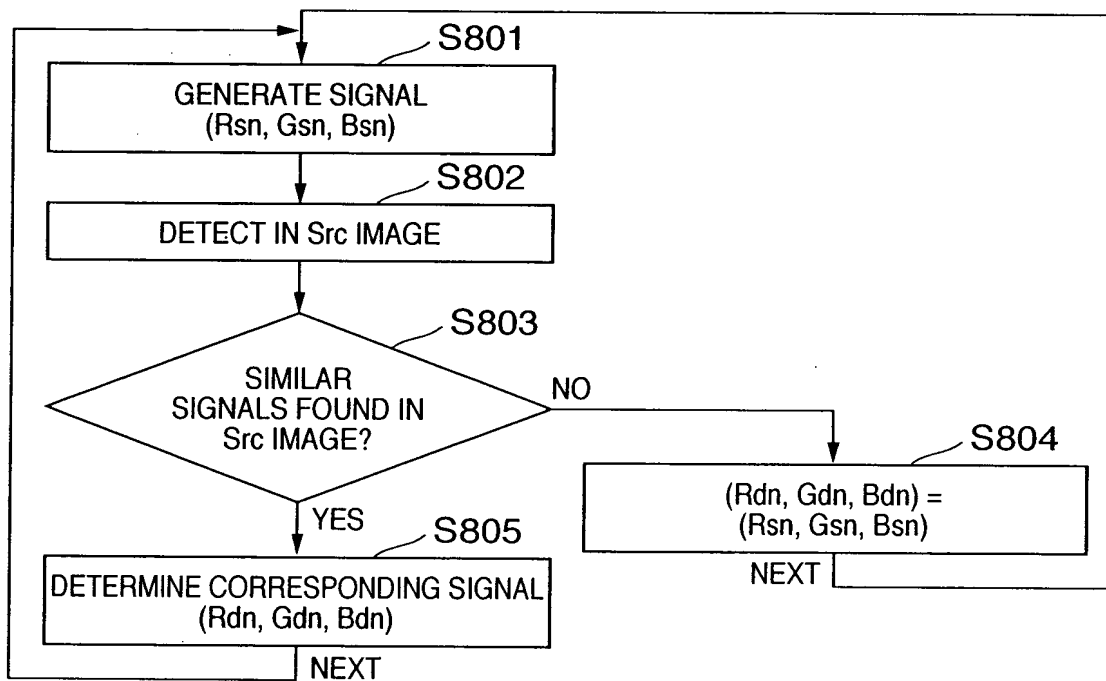


FIG. 9

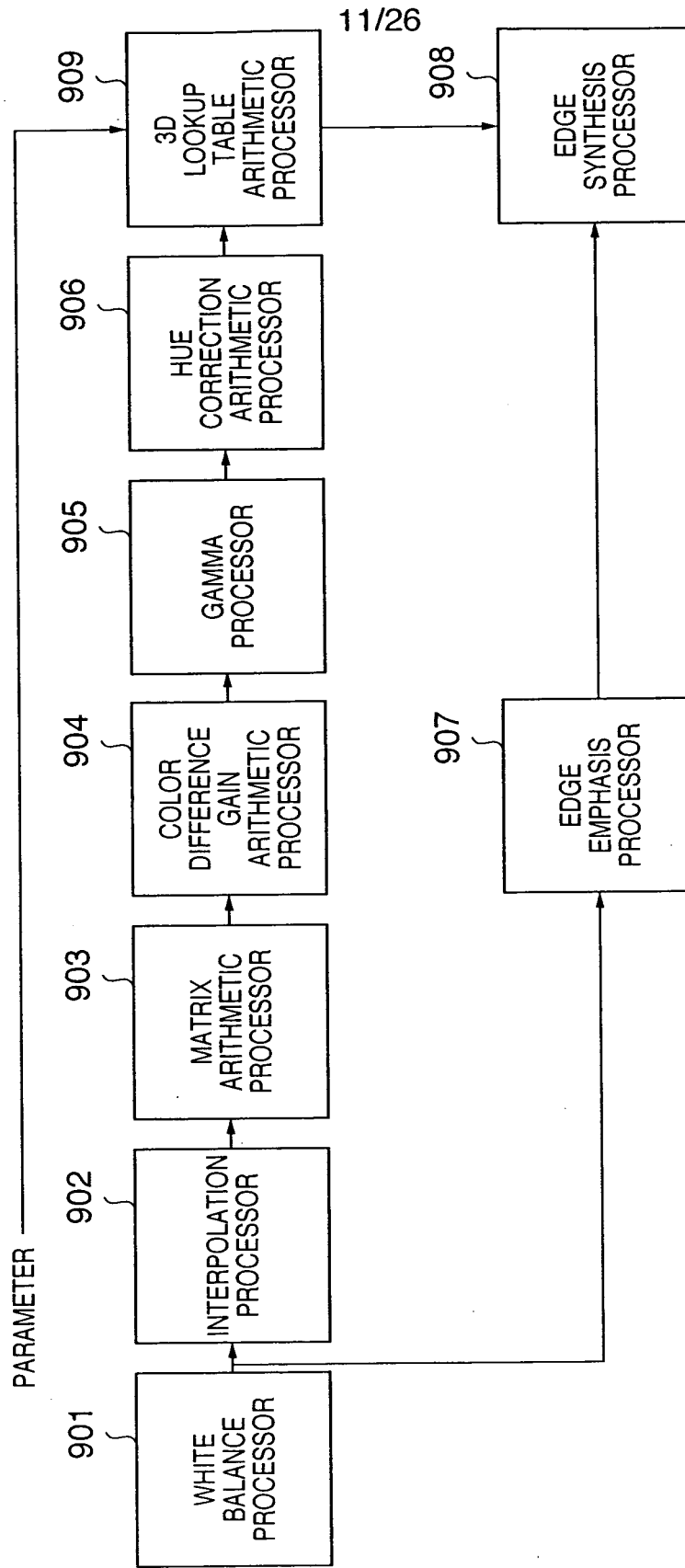


FIG. 10

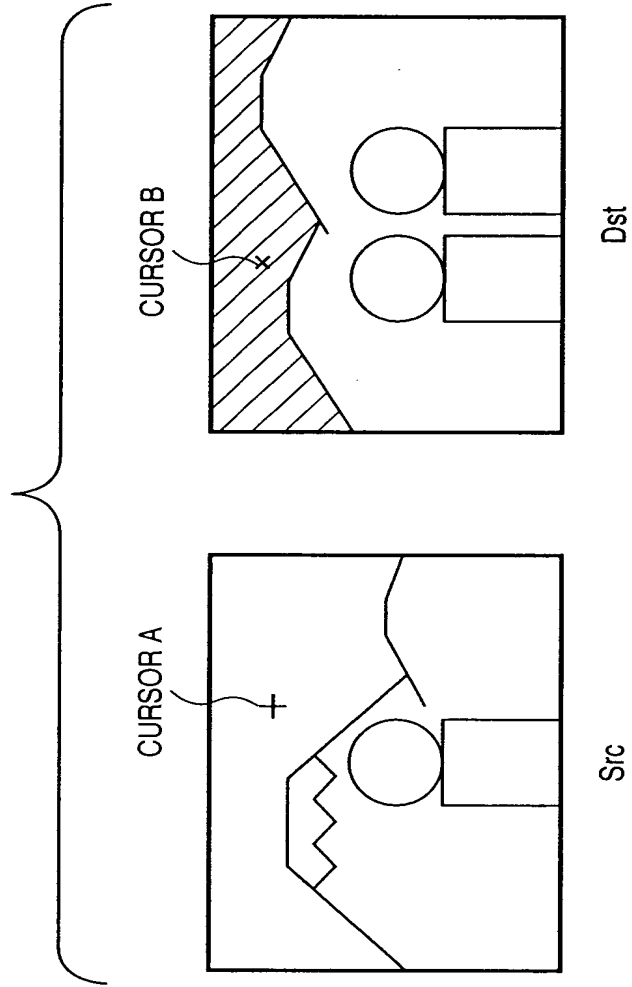


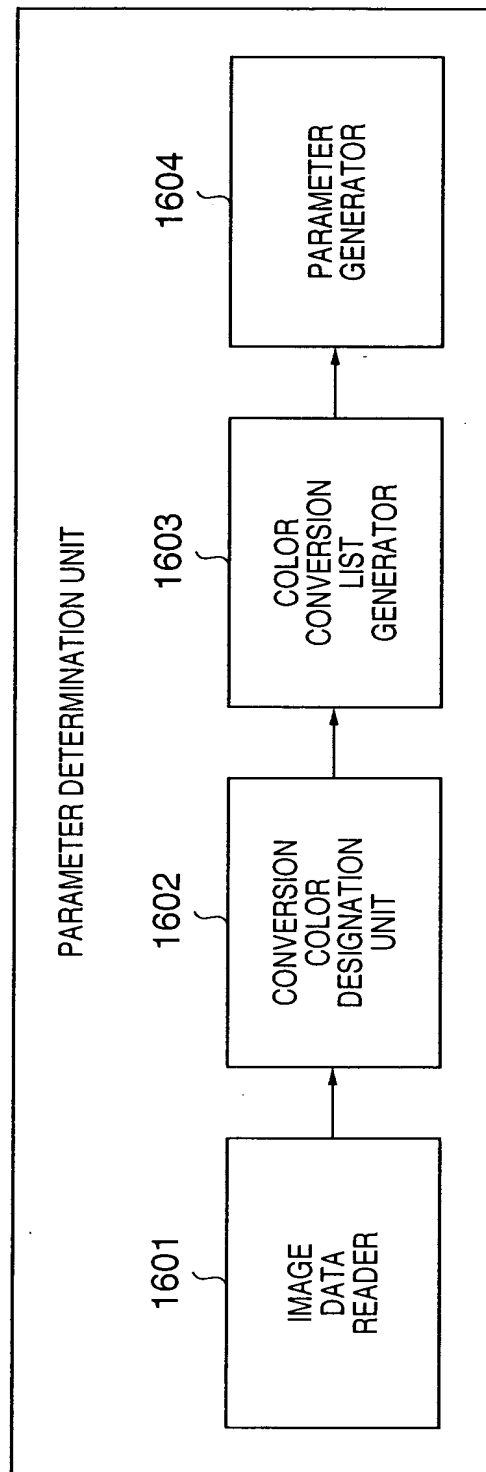
FIG. 11

FIG. 12

Src		Dst
(Rs, Gs, Bs)	→	(Rd, Gd, Bd)
(128, 192, 224)	→	(128, 180, 226)
(30, 0, 0)	→	(32, 5, 0)
(62, 0, 0)	→	(64, 0, 0)
.		.
.		.
(110, 180, 220)	→	(128, 180, 226)
.		.
.		.
(230, 255, 255)	→	(224, 255, 255)

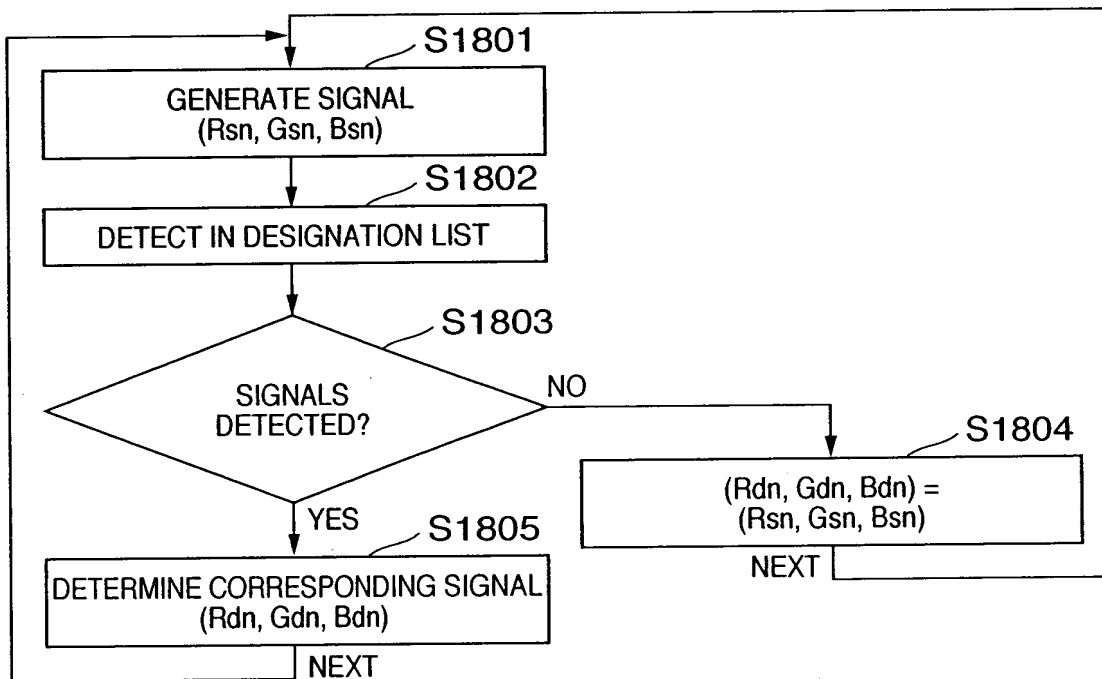
FIG. 13

FIG. 14

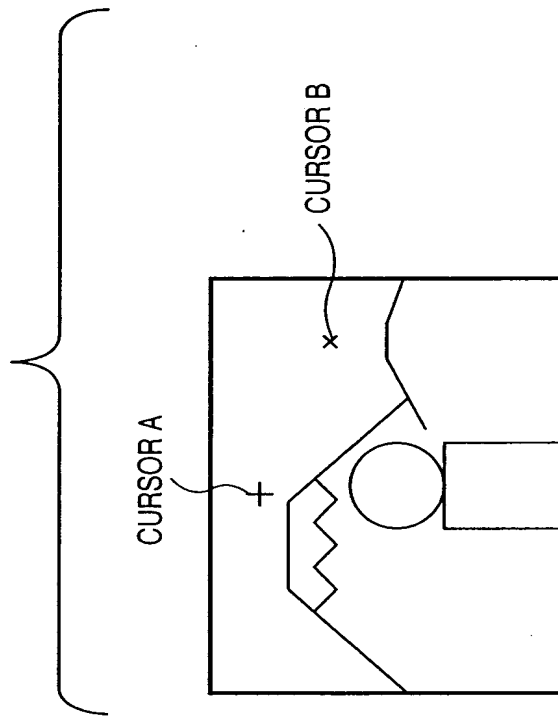


FIG. 15

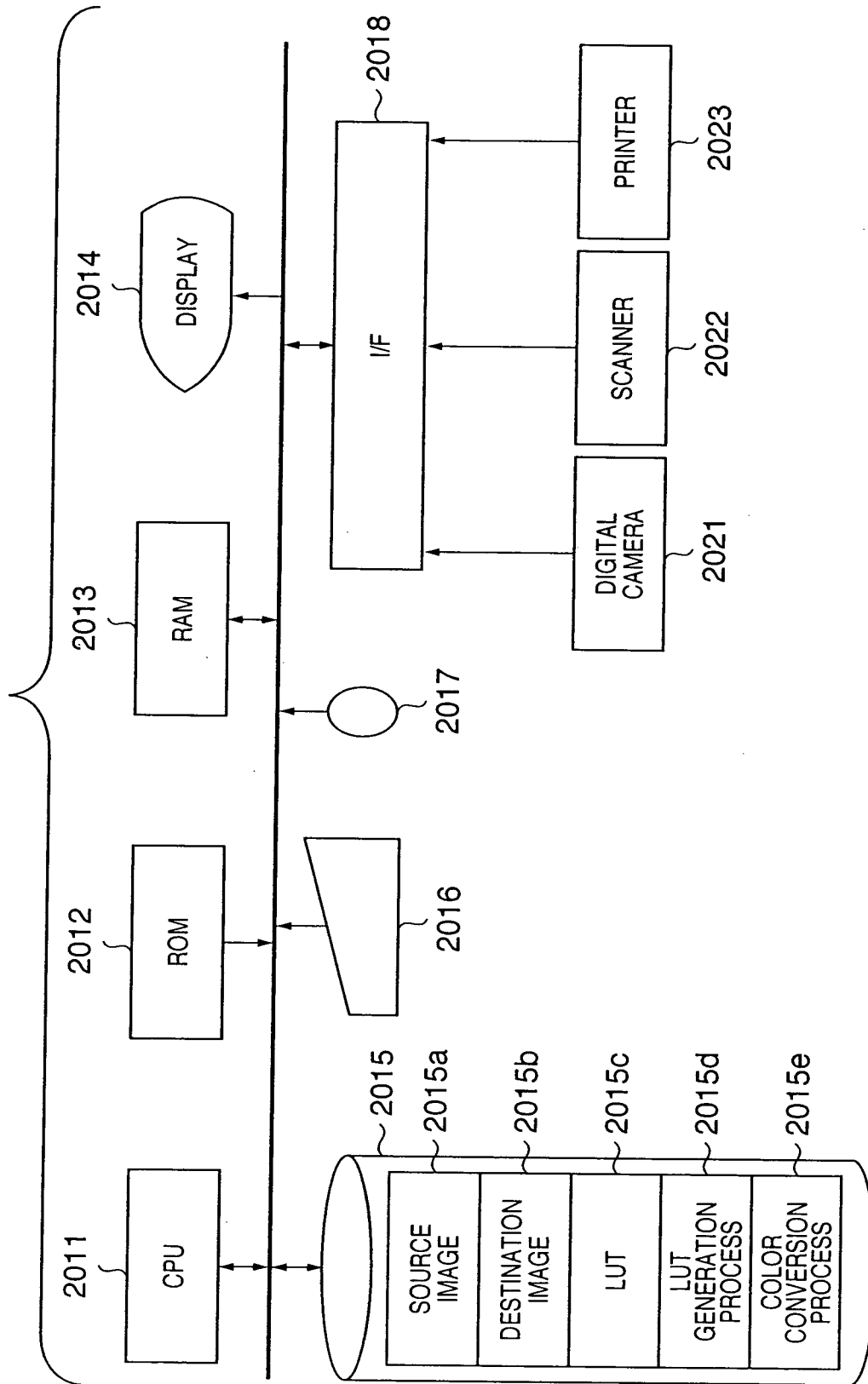


FIG. 16

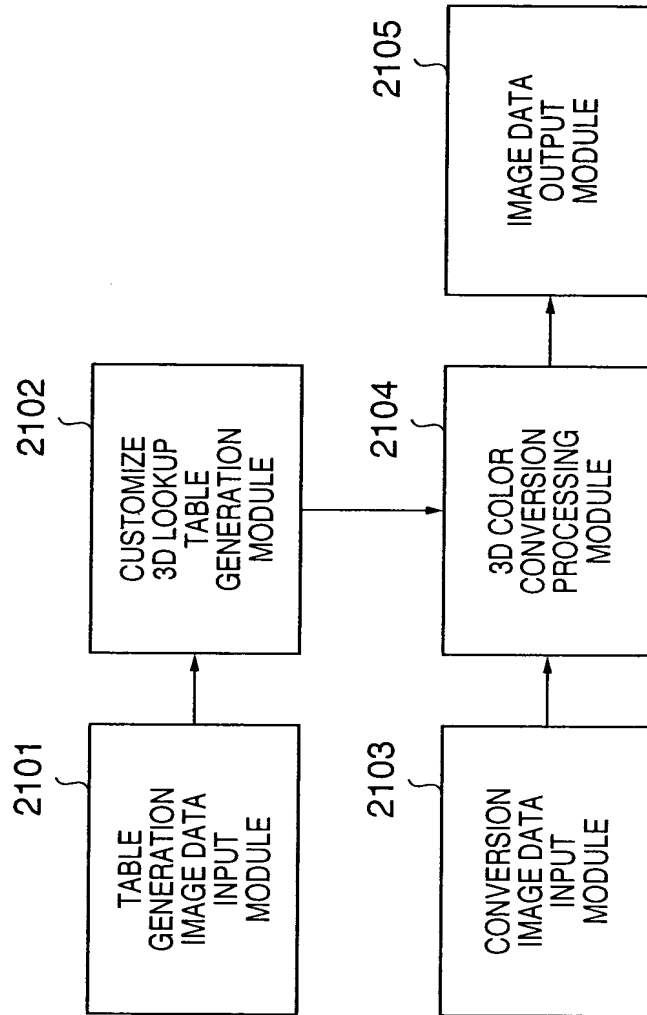


FIG. 17

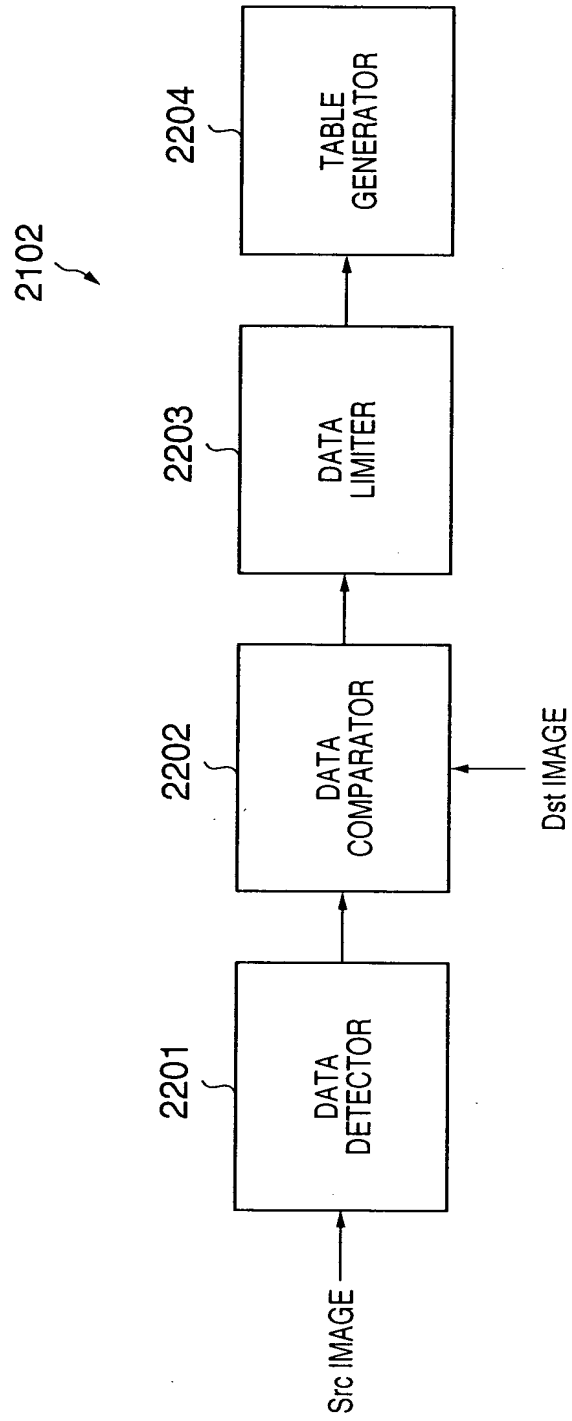


FIG. 18

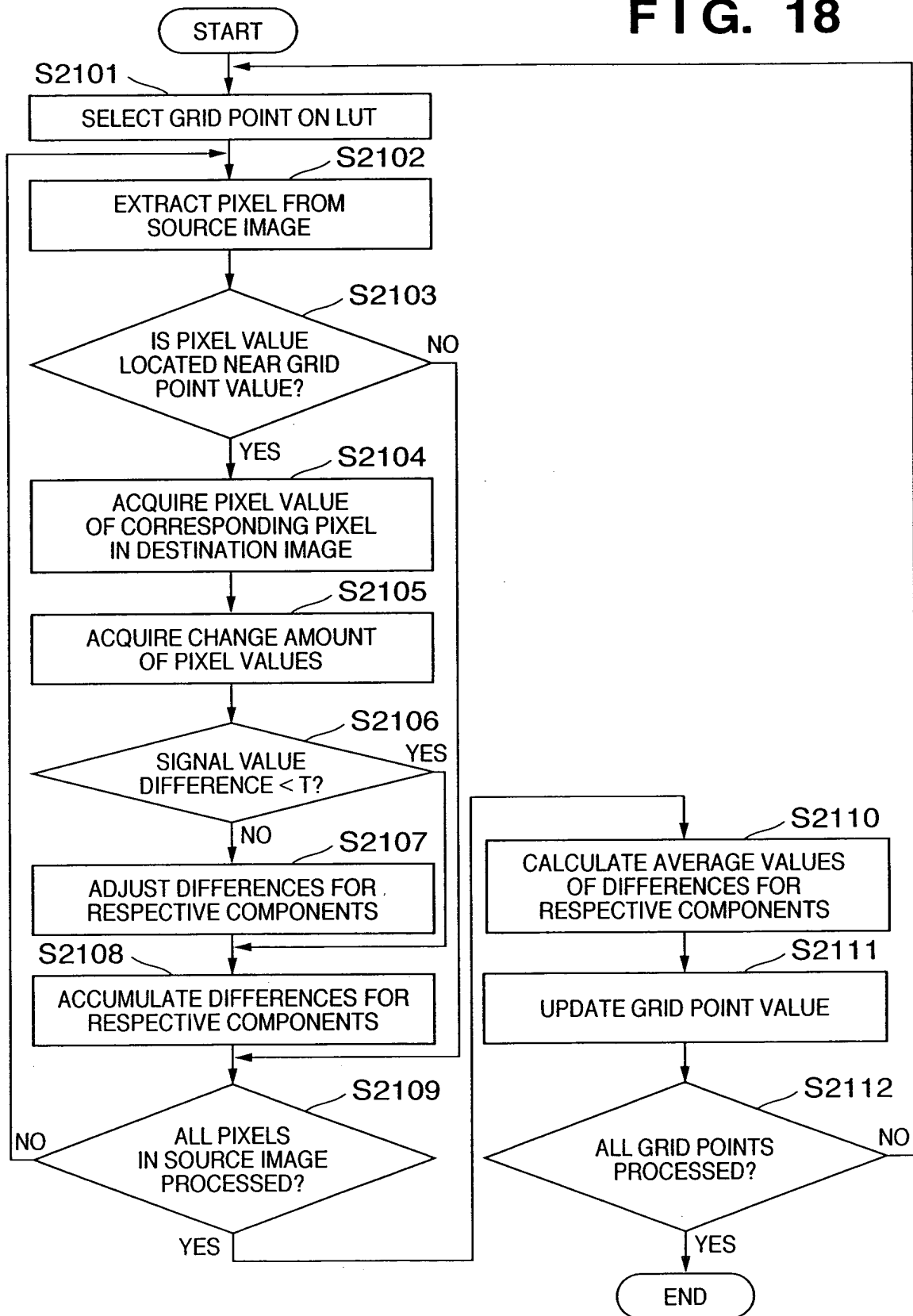


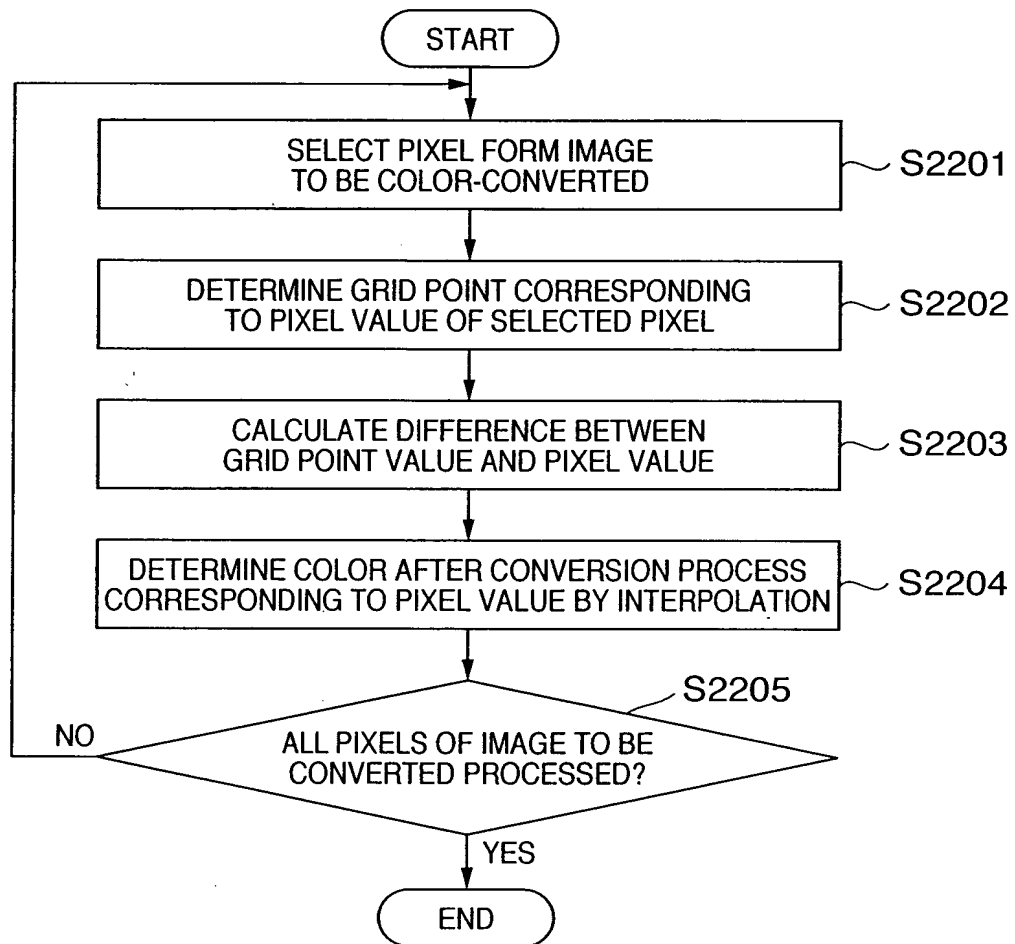
FIG. 19

FIG. 20

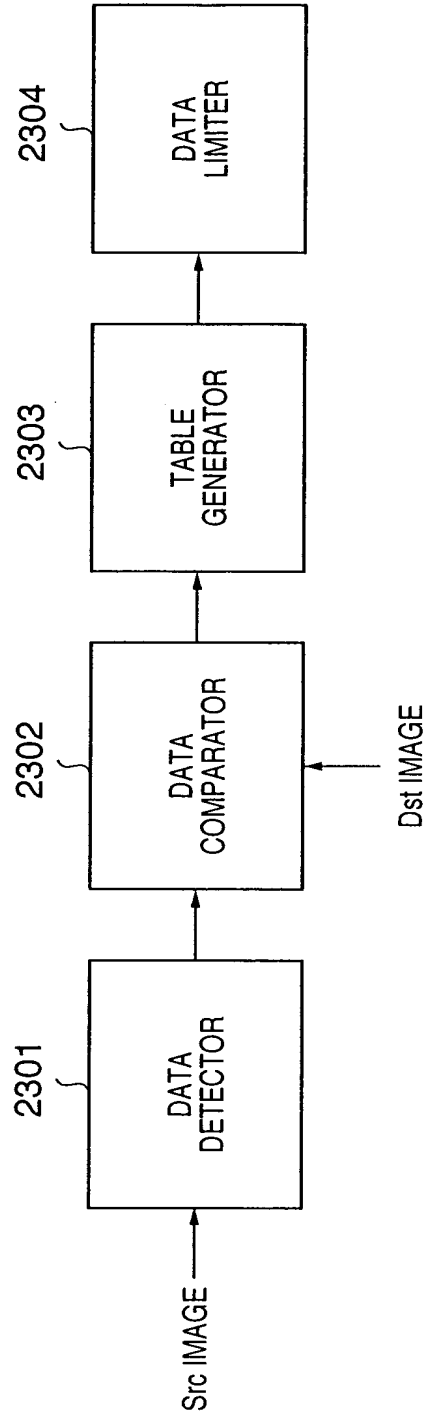


FIG. 21

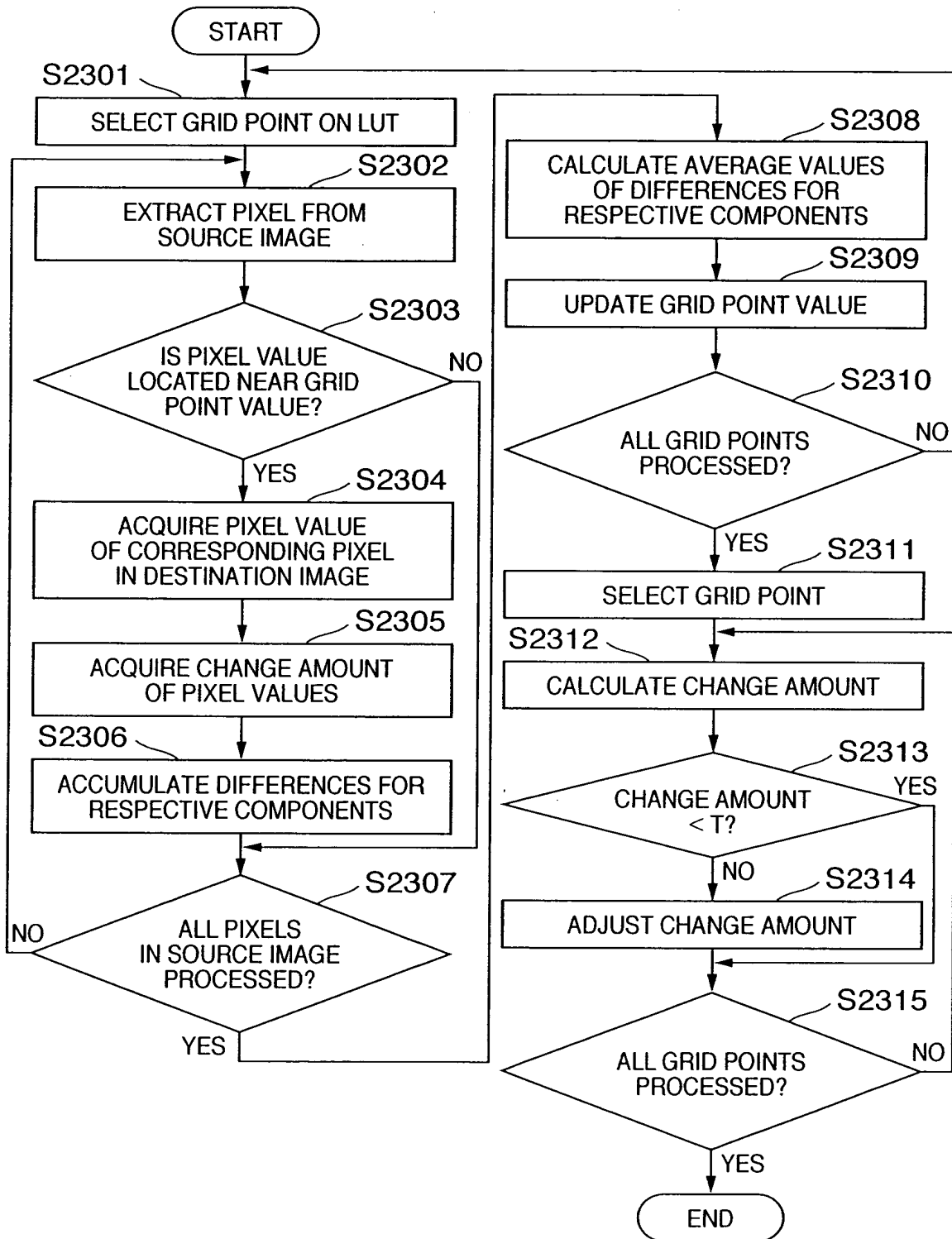


FIG. 22

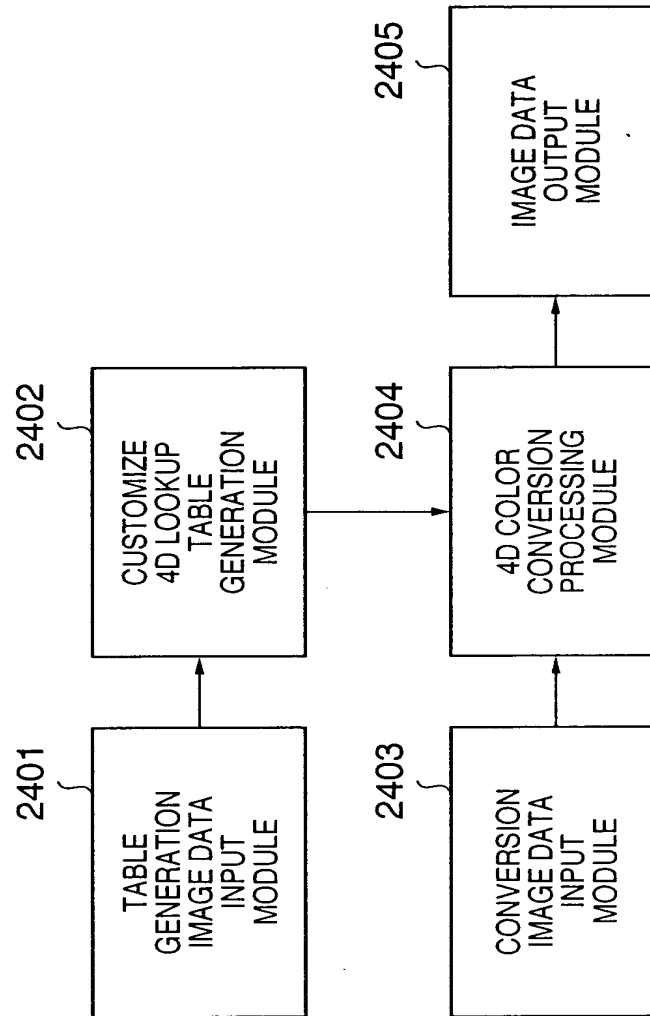
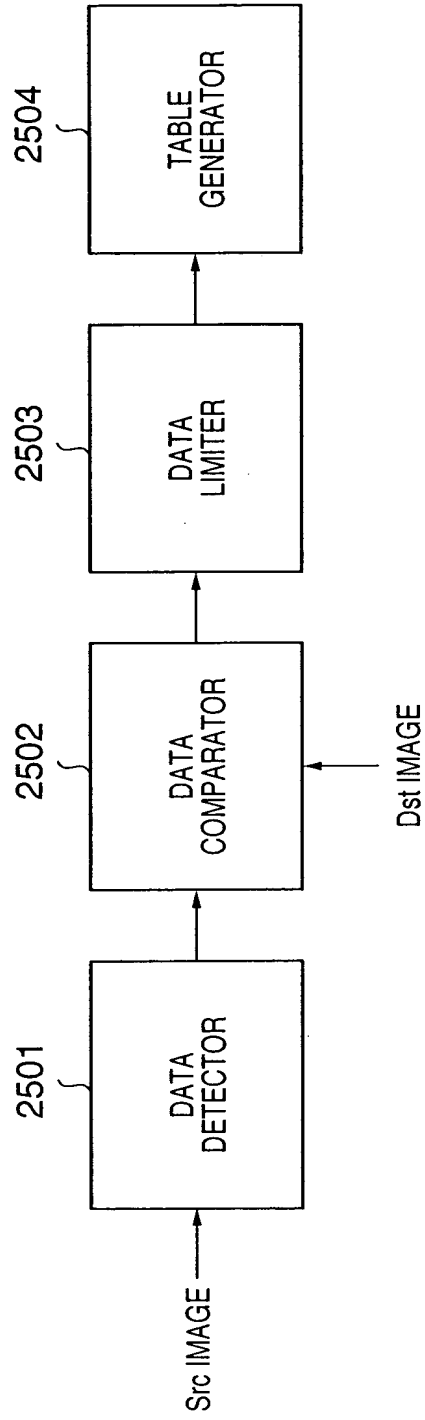


FIG. 23



[illegible]